IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	: 10/815,890	Confirmation No.	: 9079
Applicant	: Howard C. Kirsch		
Filed	: March 30, 2004	Attorney Docket No.:	501214.02 (30204/US/2)
Art Unit	: 2818	Customer No.	: 27,076
Examiner	: Trong Phan		
Title	: SYSTEM AND METHOD TO AVOID VOLTAGE READ ERRORS IN OPEN DIGIT LINE ARRAY DYNAMIC RANDOM ACCESS MEMORIES		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. § 1.111

Sir:

Please amend the above-captioned patent application as follows:

A Listing of the Claims begins on page 2 of this paper.

Remarks begin on page 8 of this paper.